IN THE CLAIMS:

1. (Cancelled).

5 2. (Cancelled).

3. (Cancelled).

4. (Previously Presented) The controller of claim 5, wherein said at least one peripheral device
10 employs said processor to perform each of said functions of said at least one peripheral device.

5. (Previously Presented) An integrated controller for use in a peripheral device for controlling
high speed communications between a host computer and said peripheral device, comprising:

      a processor integrated with said controller for controlling communications on a
15 bus using one or more communication functions, wherein said processor performs at least one
function for said peripheral device in addition to said one or more communication functions,
wherein said processor provides processing capacity for use by said peripheral device, and
wherein said high speed communications conform to a USB standard.

20 6. (Cancelled).

7. (Cancelled).

8. (Cancelled).
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9. (Cancelled)

10. (Previously Presented) The method of claim 11, wherein said at least one peripheral device
employs said first processor to perform each of said functions of said at least one peripheral
30 device.

11. (Previously Presented) A method performed by a controller on a peripheral device for controlling communications between a host computer and said peripheral device, comprising the step of:

5   executing one or more communication functions that control communications on a bus using a first processor, wherein said first processor also performs at least one function for said peripheral device in addition to said one or more communication functions, wherein said processor is integrated with said controller and provides processing capacity for use by said peripheral device, and wherein said high speed communications conform to a USB standard.

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12. (Cancelled).

13. (Cancelled).

15   14. (Cancelled).

15. (Cancelled).

16. (Cancelled)

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17. (Previously Presented) The integrated circuit of claim 18, wherein said at least one peripheral device employs said processor to perform each of said functions of said at least one peripheral device.

25   18. (Previously Presented) An integrated circuit for use in a peripheral device, comprising:

            a controller for high speed communications between a host computer and at least one peripheral device, comprising:

            a processor integrated with said controller for controlling communications on a bus using one or more communication functions, wherein said processor performs at least one

30   function for said peripheral device in addition to said one or more communication functions,

3

wherein said processor provides processing capacity for use by said peripheral device, and wherein said high speed communications conform to a USB standard.

19. (Cancelled).

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20. (Cancelled).